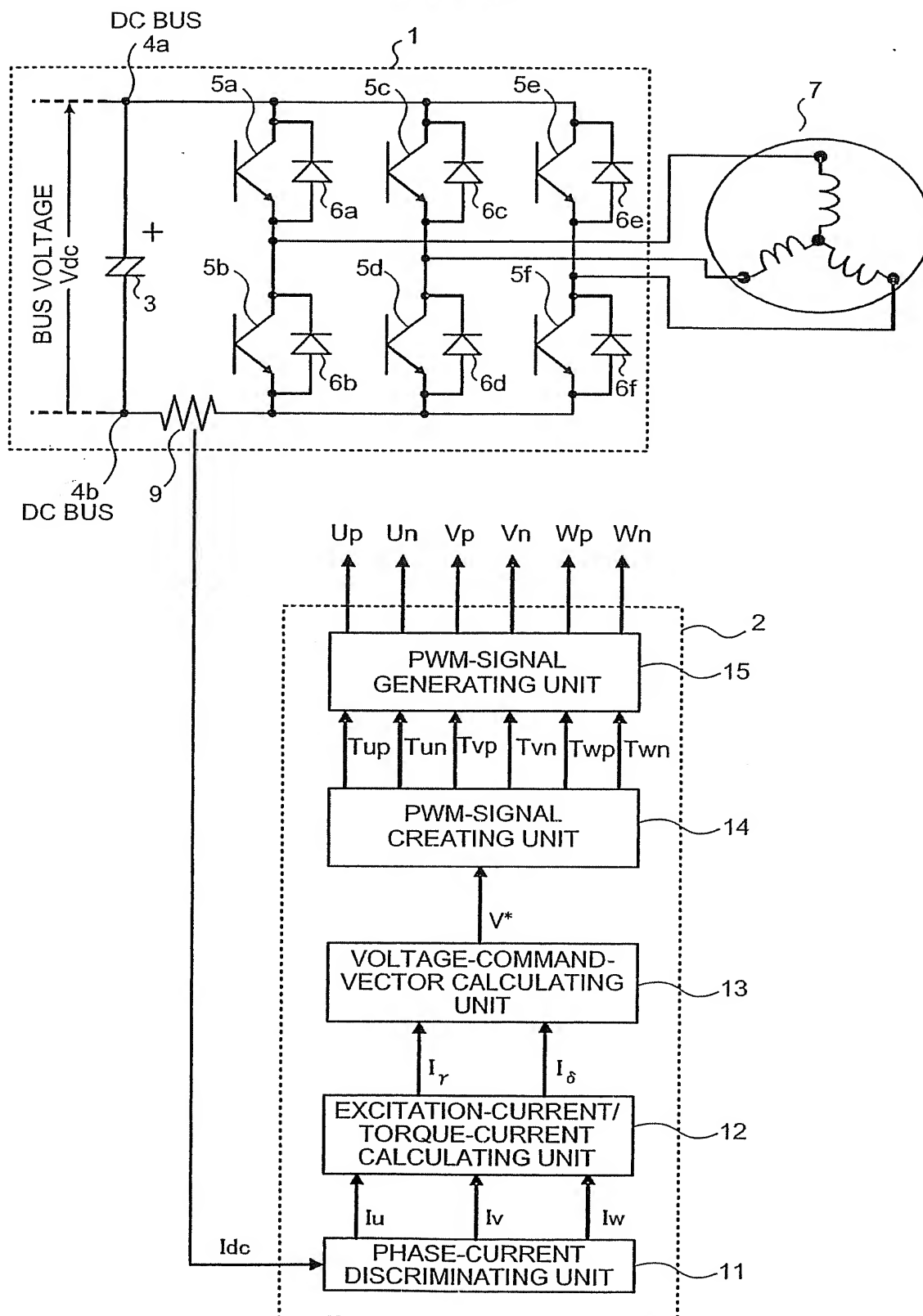


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FIG.36



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FIG.37

BASIC VOLTAGE VECTOR	W_p (W-PHASE POSITIVE SIDE SWITCHING ELEMENT LOGICAL STATE)	V_p (V-PHASE POSITIVE SIDE SWITCHING ELEMENT LOGICAL STATE)	U_p (U-PHASE POSITIVE SIDE SWITCHING ELEMENT LOGICAL STATE)	OBSERVABLE PHASE CURRENT
V0	0	0	0	OBSERVATION IMPOSSIBLE
V1	0	0	1	I_u
V2	0	1	0	I_v
V3	0	1	1	$-I_w$
V4	1	0	0	I_w
V5	1	0	1	$-I_v$
V6	1	1	0	$-I_u$
V7	1	1	1	OBSERVATION IMPOSSIBLE

CONVENTIONAL

FIG.38

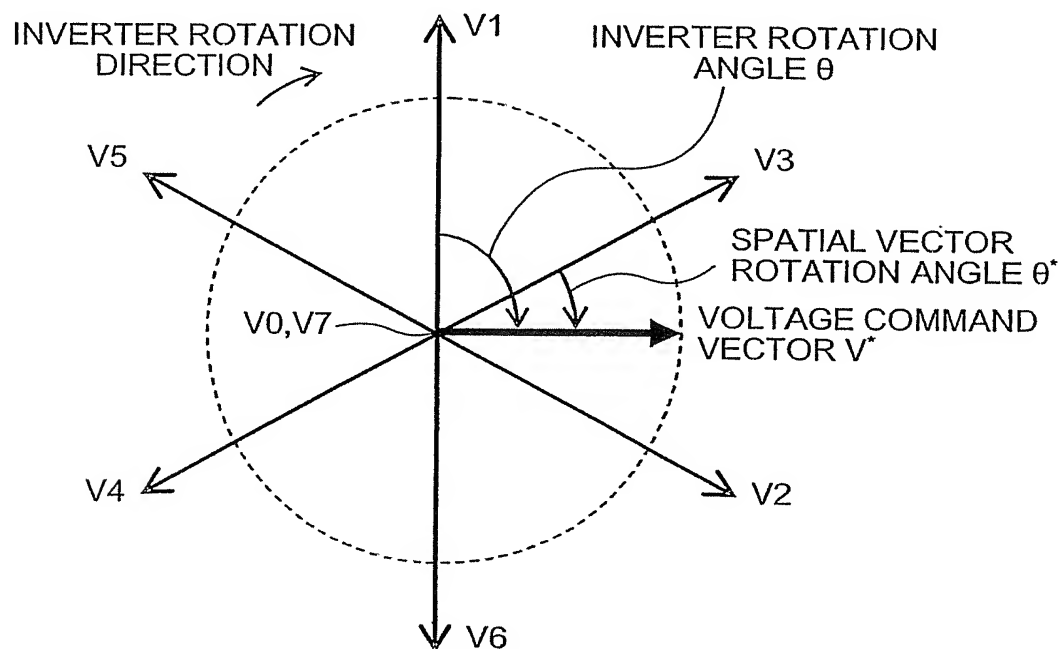


FIG.39A

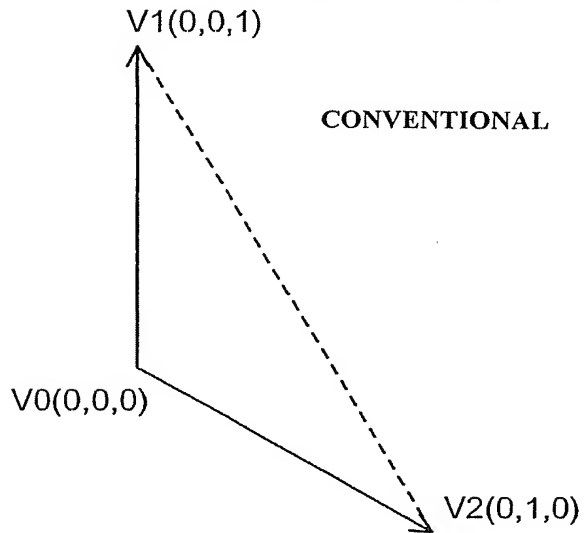
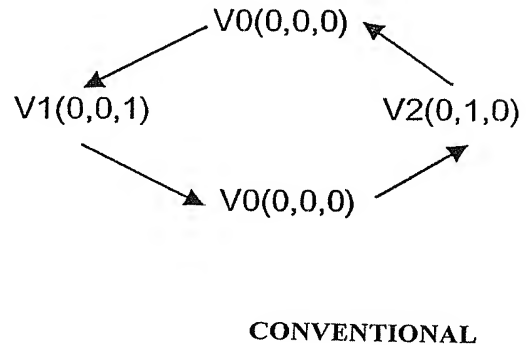


FIG.39B



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FIG.40

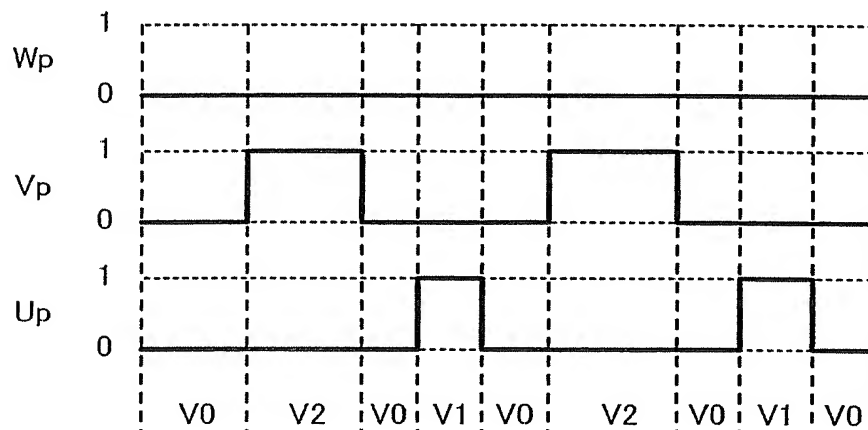


FIG.41A

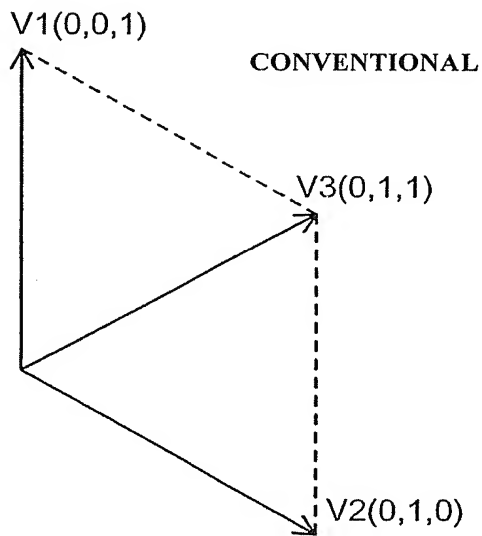


FIG.41B

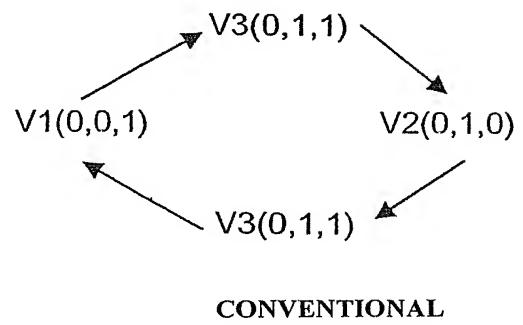


FIG.42

CONVENTIONAL

